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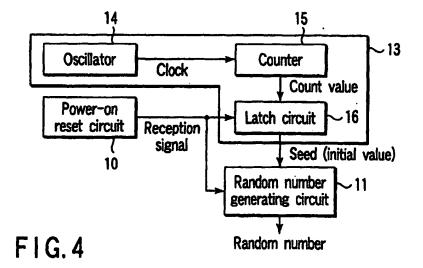
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- (54) Random number's seed generating circuit, driver having the same, and SD memory card system
- (57) A random number's seed is changed without using any nonvolatile memory every time the circuit is activated. An oscillator (14) operates immediately after the power supply is turned on to generate a high-speed clock. A counter (15) operates in accordance with the high-speed clock to change the count value at a high speed. When a sufficient time has elapsed after the power supply is turned, a power-on reset circuit (10) out-

puts a power-on reset signal indicating that the internal power supply is stabilized. The power-on reset signal is sufficiently slower than the high-speed clock. The timing at which the power-on reset signal is input to a latch circuit (16) varies. Hence, the count value (random number's seed) latched by the latch circuit (16) changes every time the power supply is turned on. A random number is generated using the random number's seed.



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## **EUROPEAN SEARCH REPORT**

Application Number EP 02 00 4378

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## ANNEX TO THE EUROPEAN SEARCH REPORT ON EUROPEAN PATENT APPLICATION NO.

EP 02 00 4378

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on The European Patent Office Is in no way liable for these particulars which are merely given for the purpose of information.

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